

PGY-STG-PA XSPI

Protocol Analyzer



XSPI-Analyzer

CONNECT TRIGGER ANALYZE REPORTS RUN

Index	StartTime	Mode	CommandType	Command	Comman	ExtCommand	ExtCom	Address	Dummy	Data
0	0	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00332F08	11	00 00 00 00 00 00 00...
1	7.50400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00133240	11	00 00 00 00 00 00 00...
2	15.00000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x003335F8	11	00 00 00 00 00 00 00...
3	22.49600us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001338B0	11	00 00 00 00 00 00 00...
4	29.99200us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001338E8	11	00 00 00 00 00 00 00...
5	38.23200us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00147A90	11	00 00 00 00 00 00 00...
6	45.72800us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00147DC8	11	00 00 00 00 00 00 00...
7	53.22400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001481B0	11	00 00 00 00 00 00 00...
8	60.72000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001484B8	11	00 00 00 00 00 00 00...
9	68.21600us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001487F0	11	00 00 00 00 00 00 00...
10	87.16000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001704D0	11	00 00 00 00 00 00 00...
11	89.94400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001306E8	11	00 00 00 00 00 00 00...
12	92.73600us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00130888	11	00 00 00 00 00 00 00...
13	95.52000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001309A0	11	00 00 00 00 00 00 00...
14	98.31200us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00130BC0	11	00 00 00 00 00 00 00...
15	101.10400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00130CD8	11	00 00 00 00 00 00 00...
16	103.88800us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00130EF8	11	00 00 00 00 00 00 00...
17	106.68000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x0013E090	11	00 00 00 00 00 00 00...
18	109.50400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x0013E1B0	11	00 00 00 00 00 00 00...
19	121.32000us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00128AD8	11	A8 DC A8 DC 58 63 A7 D0...
20	122.97600us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00128BF8	11	A7 D0 A7 D0 FF 63 A7 D0...
21	124.56800us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00128D90	11	A7 D0 A7 D0 FF 63 A7 D0...
22	127.84800us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001290C8	11	A7 D0 A7 D0 FF 63 A7 D0...
23	129.50400us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x001292E8	11	A7 D0 A7 D0 FF 63 A7 D0...
24	131.09600us	8-DDR	ReadCommand	8 I/O DT Read	0xEE	8 I/O DT Read	0x11	0x00129480	11	A7 D0 A7 D0 FF 63 A7 D0...
25	134.37600us	8-DDR	ReadCommand	8 I/O DT Read	0xFF	8 I/O DT Read	0x11	0x001297B8	11	A7 D0 A7 D0 FF 63 A7 D0...

Progress: 0

PGY-STG-PA XSPI Protocol Analyzer is the most feature rich comprehensive Protocol validation product available to validate, analyze and debug XSPI/OSPI Protocol layer. PGY-STG-PA XSPI Protocol Analyzer supports XSPI Protocol analysis for wide data rate up to 250MHz DDR mode. The innovative active probe has minimum electrical loading on device under test (DUT) and allows the protocol data capture without affecting the performance DUT. PGY-STG-PA Protocol analyzer allows industry first continuous streaming of protocol data to host system.

PGY-STG-PA XSPI Protocol Analyzer allows Design and Test Engineers to test and debug XSPI interface by capturing the XSPI protocol activity from power on state and full transfer while the device under test switching from one lane to eight lane mode and analyzes the protocol errors. This helps the design and test engineer debug design issues and reducing the time to market.

Key Features and benefits of Protocol Analyzer

- Provides industry best probing solutions to simultaneously capture commands, response and data in 1S-1S-1S and 8S-8S-8S mode.
- Data capture at 250MHz
- Continuous streaming of protocol data to host system SSD/HDD for long duration
- Hardware based protocol aware trigger capability enables capturing specific events
- On the fly protocol validation for any failure in sequence of expected commands and response
- Analytical information about commands, response, and data
- Analytics provides the decoding of device registers for easy analysis
- Filter feature allows you to view specific packets in decoded protocol packets
- Search for specific events in protocol activity
- Easy to use software user interfaces reduces the learning curve of protocol analysis
- Software is designed to handle long duration capture and display the decoded data without demanding extensive resources in host computer
- Trigger out signal for any specific protocol event allows triggering of other instruments such as oscilloscope
- Interface to host system using USB3.0 interface
- Flexibility to upgrade the hardware firmware provides easy field up gradation of firmware
- Decoded data packets can be exported to txt file for further analysis
- Upgradable to support QSPI, eMMC, SD and SDIO Protocol Analysis
- Report Generation
- API Support

Specifications for XSPI/OSPI Protocol Analyzer:

Interfaces Supported	XSPI and OSPI
Protocol Decode	Command, Response, CRC, Data, Arguments, Device registers
Data Decode	1 bit, 8 bit
Timing Analysis	Asynchronous high speed data capture to analyze the timing issues
Operating Voltage levels	1.2V, 1.8V and 3.3V
Storage Capability	Continuous streaming of acquired protocol to host computer storage system
Simultaneous monitoring of devices	Two
Capture Mode	Manual Run/Stop, Time specific
Trigger on	Command, Response, If-then-else multilevel trigger capabilities
Trigger Actions	Capture data and/or trigger out signal
Signal Input	Digital Signal input to mark the activities in Protocol activity
Host System Interface	USB3.0

Power Advanced Trigger Capabilities

XSPI-Analyzer

CONNECT

TRIGGER

ANALYZE

REPORTS

RUN

TRIGGER MODE

☐ Auto
☐ Message
☒ Advanced

☐ TRIGGER-IN

☒ LowToHighEdge
☐ HighToLowEdge
☐ HighLevel
☐ LowLevel

Advanced Trigger

Level Count 2

Level # 0

If

Start

Then

Command

Address(3-4 Bytes)

Data(5 Bytes)

Action

Nothing

Go to Level

1

☐ Else If

Start

Then

Command

Address(3-4 Bytes)

Data(5 Bytes)

Action

Nothing

Go to Level

1

Level # 1

If

Start

Then

Command

Address(3-4 Bytes)

Data(5 Bytes)

Action

Nothing

Go to Level

0

☐ Else If

Start

Then

Command

Address(3-4 Bytes)

Data(5 Bytes)

Action

Nothing

Go to Level

0

Progress: 0

PGY-STG-PA software supports auto, message and advanced trigger capabilities. This enables design engineer to capture elusive event with easy by setting trigger events. If-then-else if trigger feature monitors two trigger events and takes the action. This action could be to monitor another trigger event or capture the protocol activity from the device under test.

Host Machine Minimum Requirements:

Computer hardware with Microsoft Windows®7/8/10/11 OS, 16GB RAM, Storage with at least 50GB free hard disk space/SSD for storing acquired data

Ordering information:

PGY-STG-PA XSPI /OSPI Protocol Analyzer

(Shipment includes Hardware, software CD, one set probe, USB3.0 , power adopter)

Options:

Opt. 1001 Active Probes (Flying lead probe with female header pins)

Warranty:

Hardware and software carries a warranty of one year.

Probes are covered three-month warranty for any manufacturing defects

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About Prodigy Technovations Pvt

Prodigy Technovations is the leading provider of innovative protocol analysis solutions for mainstream and emerging technologies. We provide Protocol Decode, and PHY layer testing solutions on Test & Measurements equipment's. The company's ongoing efforts include successful implementation of innovative and comprehensive protocol Analysis solutions using latest hardware technologies.

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