

Universal Access Device 2^{next}

Universal Access Device 2^{next} – All-Rounder for Debugging and Trace

The **Universal Access Device 2^{next} (UAD2^{next})** is the new all-in-one device in PLS's UDE target access device family. It combines the state-of-the-art debugging features of the UAD2^{pro} with trace capabilities, which makes it ideal for efficient debugging, test and system-level analysis. Together with the **Universal Debug Engine® (UDE)** the UAD2^{next} provides a comprehensive and powerful support even for the latest heterogeneous multi-core SoCs.

- Fast and reliable access to AURIX TC2xx and TC3xx, TriCore, PowerArchitecture, Cortex-R, Cortex-M, Cortex-A, XC2000, XE166, XMC1000, XMC4000, ARM7/9/11, SuperH SH-2A and RH850.
- Proven and robust aluminum housing 17.0 x 14.5 x 5.5 cm.
- Passive Cooling.

High-Speed Target Access

The UAD2^{next} is optimized for high-speed debug communication between UDE running on the host PC and the target system.

- Proven target adapter solution already used for UAD2^{pro} and UAD3+ offers fastest and reliable target access for state-of-the-art debug interfaces **DAP, SWD, JTAG, cJTAG, LPD**.
- Ready for upcoming debug interfaces without replacing the base UAD2^{next} device.
- High-speed debug access with up to **160 MHz shift clock** and **1.65 V – 5.5 V I/O voltage**.
- Galvanic isolated target adapters (RF coupler technology with 1,000 V_{RMS} isolation) available.
- Longer distances between UAD2^{next} and target system. Up to 0.5 m possible, 2.0 m on request.
- Connectors for **ASC** and **CAN/CAN FD^{*)}** with galvanic signal isolation up to 1,000 V_{RMS}.
- Support for **DXCPL** (DAP over CAN Physical Layer).
- **USB3** or **Gigabit Ethernet** for connecting UAD2^{next} to the PC.

At a Glance – The Universal Access Device 2^{next}

- High performance debug access to AURIX TC2xx and TC3xx, TriCore, PowerArchitecture, Cortex-R, -M, -A, XC2000, XE166, XMC4500, ARM7/9/11, SuperH SH-2A, RH850
- ASC and CAN/CAN FD^{*)} target interfaces
- Plug-in modules for parallel and serial trace
- 512 Mbyte internal trace memory
- USB3 and Gigabit Ethernet for connection to PC



Trace Option

For trace based debugging, measurement and system-level analysis the UAD2^{next} can be extended with target specific trace modules.

- Easy mounting plug-in modules for a wide range of trace interfaces.
- Up to 12 bit parallel trace.
- 2 Lane serial trace for up to 1.25 GBit/s.
- 512 Mbyte internal trace memory.
- Ready for ARM CoreSight ETM, STM, ITM, PTM, Xilinx FTM, for NEXUS class 3 parallel / serial AURORA trace and for Infineon MCDS.

^{*)} CAN FD available upon request.

If you have any questions about our products, please feel free to contact us:

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