

# Universal Access Device 2<sup>pro</sup>

## Universal Access Device 2<sup>pro</sup> - Smart Communication Accelerator

The **Universal Access Device 2<sup>pro</sup>** (UAD2<sup>pro</sup>) is the smart member of the UDE target Access Device family and offers state-of-the-art debug features as well as an additional CAN target communication channel. The UAD2<sup>pro</sup> consequently relies on the proven target adapter solutions of the Universal Access Devices family. Together with the **Universal Debug Engine® (UDE)** the UAD2<sup>pro</sup> provides a comprehensive and powerful support even for the latest multi-core SoCs.

- Efficient and reliable access to **AURIX 2nd Generation, AURIX, TriCore, PowerArchitecture, Cortex-R, Cortex-M, Cortex-A, XC2000, XE166, XMC1000, XMC4000, ARM7/9/11, SuperH SH-2A and RH850.**
- Fastest target access via **JTAG, cJTAG, DAP, DAP over CAN Physical Layer (DXCPL), SPD (Single Pin DAP) via CAN, SWD, ASC and CAN**
- Build-in ESD protection.
- Proven and robust aluminum housing (8.5 x 13 x 3.5 cm)

### High-Speed Target Access

The UDE2<sup>pro</sup> is optimized for high-speed debug communication between the UDE running on the host PC and a target system.

- Debug communication channel supporting up to **50 MHz shift clock** – download rate up to **3.5 MByte/s**
- **1.65V – 5.5V I/O voltage**
- Support for **JTAG, cJTAG, DAP, DAP over CAN Physical Layer (DXCPL), SPD via CAN, SWD, LPD**
- Target specific adapters for all supported devices and interfaces
- Galvanic isolated target adapters (RF coupler technology with 1,000 VRMS isolation) available.
- Lowest power consumption from target
- Flexible serial high-speed communication to a XC2000, XE166, TriCore, PowerArchitecture and ARM/Cortex based target system via a D-Sub connector. The following serial modes are available:
  - Asynchronous serial **RS232** interface
  - **CAN** interface
- **USB 2.0** for connecting UAD2pro to the PC
- Supported OS (32 and 64 bit): Windows™ 7, Windows™ 8, Windows™ 10.



### CAN Bus Analyzer

- CAN bus D-Sub male connector (CiA pin assignment) as debugging communication channel to C166, ST10, XC2000, XE166, TriCore, PowerArchitecture and ARM/Cortex based CAN target systems
- Continuous trace of CAN bus messages
- CAN bus analyzing may run as background task of debug communication.

**If you have any questions about our products, please feel free to contact us:**

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